



Sheet 1 of 1

INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION
(PTO-1449)

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KUB-001U.S. PATENT
APPLICATION NO.
10/511,720APPLICANT
Kanji OTSUKA et al.FILING DATE
October 18, 2004

GROUP

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
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he	Masahiro Murakawa, Yuji Kasai, Eiichi Takahashi, Hidenori Sakanashi, Neil Marston, Kiryu Shogo and Tetsuya Higuchi, "Analog Evolvable Hardware and Its Applications," Technical Report of the Institute of Electronics, Information and Communication Engineers, Vol. 100, No. 89 (May 19, 2000), pp. 1-8 (AI2000-10)
W	Isamu Kajitani, Tsutomu Hoshino, Masahiro Murakawa and Tetsuya Higuchi, "Implementation of the Circuit for Determining the Neural Network Structure with GAs," Japanese Neural Network Society Journal, Vol. 5, No. 4 (December 5, 1998), pp. 145-153
W	Kenichi Itoh, "Science and Technology," Nikkan Kogyo Shimbun, Ltd., July 28, 1998, pp. 74-88
W	Eikichi Yamashita, Hideyuki Ohashi and Kazuhiko Atsuki, "Simple CAD Formulas of Edge-Compensated Microstrip Lines," 1989 IEEE MTT-S International Microwave Symposium Digest (13-15 June 1989), pp. 339-342
EXAMINER	DATE CONSIDERED
DOW	5/17/07

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